## BINDURA UNIVERSITY OF SCIENCE EDICATION FACULTY OF SCIENCE EDUCATION

**DEPARTMENT OF ENGINEERINGAND PHYSICS** 

Bachelor of Science Honours Degree in Electronic Engineering

EEN3206 - Computer Architecture and Operating Systems

Time Allowed: 3 Hours Total Marks: 100

Special Requirements: Scientific Calculator, rule, pen, pencil

## **INSTRUCTIONS**

1. Answer any FIVE (5) questions

2. The question paper contains SEVEN (7) questions

2. Each question carries 20 marks

1(a)Explain the following advantages of computer networking (i)Data sharing (b)Given 192.168.10.10 IP address (i) Write down the host IP address (ii) Write the default network address (iii)Write down the default Subnet Net Mask (c)Explain the following types of network security (i) VPN (ii)Firewalls (d)Which two protocols at layer 4 of the OSI layer (e)Discuss the following network topologies (i)Ring Topology (ii)Mesh Topology (f)Discuss the following network (i)VLAN	[2] [1] [1] [1] [2] [2] [2] [3] [3]	٩	
<b>2(a)(i)</b> A process of size 200 MB needs to be swapped into the hard disk. But there is no space in memory. A process of size 250 MB is lying idle in memory and therefore, it can be swapped out. How much swap time is required to swap-in and swap-out the processes if:			
<ul> <li>Average latency time of hard disk = 10 ms</li> <li>Transfer rate of hard disk = 60 MB/s</li> <li>(ii)Three processes P1, P2, and P3 of size 21900, 21950, and 21990 bytes respectively, need space in the memory. If equal-sized partitions of 2200 allocated to P1, P2, and P3, will there be any fragmentation in this allocation working.</li> <li>(b) Discuss the following memory allocation techniques</li> </ul>	00 bytes a	are	
(i)First-fit allocation (ii)Best-fit allocation (iii)Worst-fit allocation (c)When is noncontiguous preferable to contiguous memory allocation? (d)Let us assume that the user process is of size 2048KB and on a standar where swapping will take place has a data transfer rate around 1 MB per Calculate the time taken for actual transfer of the 1000K process to or fit	second.		
<ul><li>3(a)State the difference between a process and a program in terms of resource Program does not compete for resources while a process compete for resources.</li><li>(b)What are the three main purposes of an operating system?</li></ul>			
<ul> <li>(c)(i)The value of effective access time in demand paging should be at a minimal better performance." Justify the statement.</li> <li>(ii)Why is the demand paging method more popular than demand segmental implementing virtual memory?</li> <li>(d)Consider a reference string 2,1,3,4,1,5,3,2,1,4,5. Suppose there are three forms.</li> </ul>	[2] tion in [2]	ş in	

memory initially. All frames are empty at the beginning. Calculate the total page faults and page replacements using FIFO.  (e)When does a page fault occur?	[9]
<ul> <li>4(a)Discuss the following Single user operating systems</li> <li>(i) Single-User Single-Tasking operating system</li> <li>(ii) Single-User Multi-Tasking operating system</li> <li>(b)State three features of single user operating systems</li> <li>(c)State three examples of single user operating systems</li> </ul>	[2] [2] [3] [3]
<ul> <li>(d)State four functions of single user operating systems.</li> <li>(e)What are the advantages of single user operating systems</li> <li>(f)Explore the following disadvantages of single user operating systems</li> <li>(i) Idle time is higher</li> <li>(ii) Tasks take longer to complete</li> </ul>	[4] [4] [1] [1]
<ul><li>5(a)(i)Why are multiprocessors useful?     (ii)How do the responsibilities of a multiprocessor operating system differ a uniprocessor system?</li><li>(b)Discuss the following three types of multi-processor architectures with the diagrams.</li></ul>	[2]
<ul> <li>(i) Uniform Memory Access Architecture</li> <li>(ii)Non-Uniform Memory Access Architecture</li> <li>(c) With the aid of a diagram explain the following multiprocessor organisation</li> <li>(i) Master-Slave multi-processor organization/configuration</li> <li>(ii)Separate kernel organisation/configuration</li> </ul>	[4] [4] IS [4] [4]
<ul> <li>6(a)(i) What are the necessary conditions which should be present for deadlock (b)Why is the occurrence of deadlocks unfavourable in a system?</li> <li>(c) There are various ways to handle deadlock. Different operating systems for methods to deal with the problem of deadlock. Discuss the following commethods.</li> <li>(i)Deadlock prevention</li> <li>(ii)Deadlock avoidance</li> <li>(iii)Deadlock detection</li> </ul>	[3] bllow different nonly used [3] [3]
(iii)Deadlock detection (d)A process has relocatable code of size of 900 K. The relocation register is I 40020 K and the limit register contains the address 41000 K. If the processor go logical address 990, where will it be located in the physical memory?	
<ul> <li>7(a)(i)Discuss the salient features of Von Neumann concept in computer design (b)Show with diagram the implementation of this concept in a real computer which has been produced in those days.</li> <li>(c)What are the limitations of Von Neumann concept (also known as Von Neum bottleneck)?</li> <li>(e) Discuss how modern days computers have evolved since Von Neumann's im of the computer.</li> </ul>	system [5] nann's [6]

